

Figure 1 Defect Density vs. Defect Size

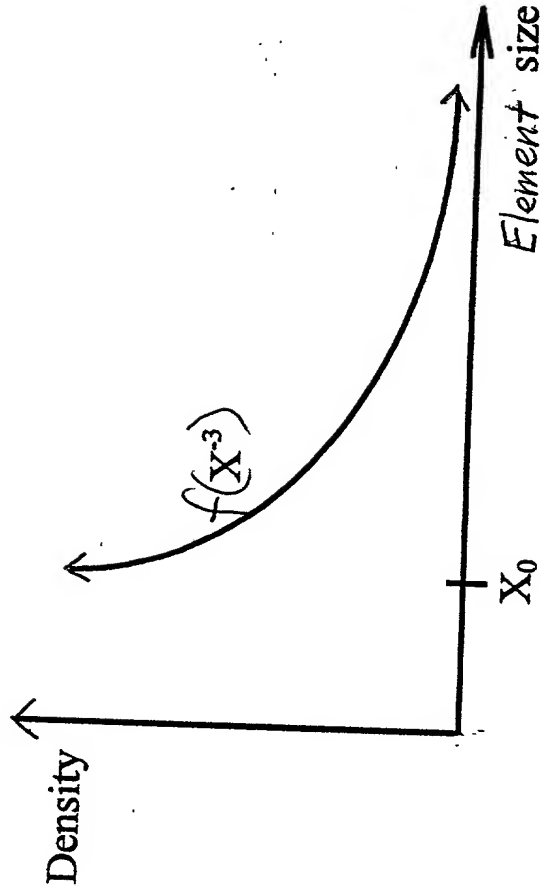


Figure 2: Array Block diagram

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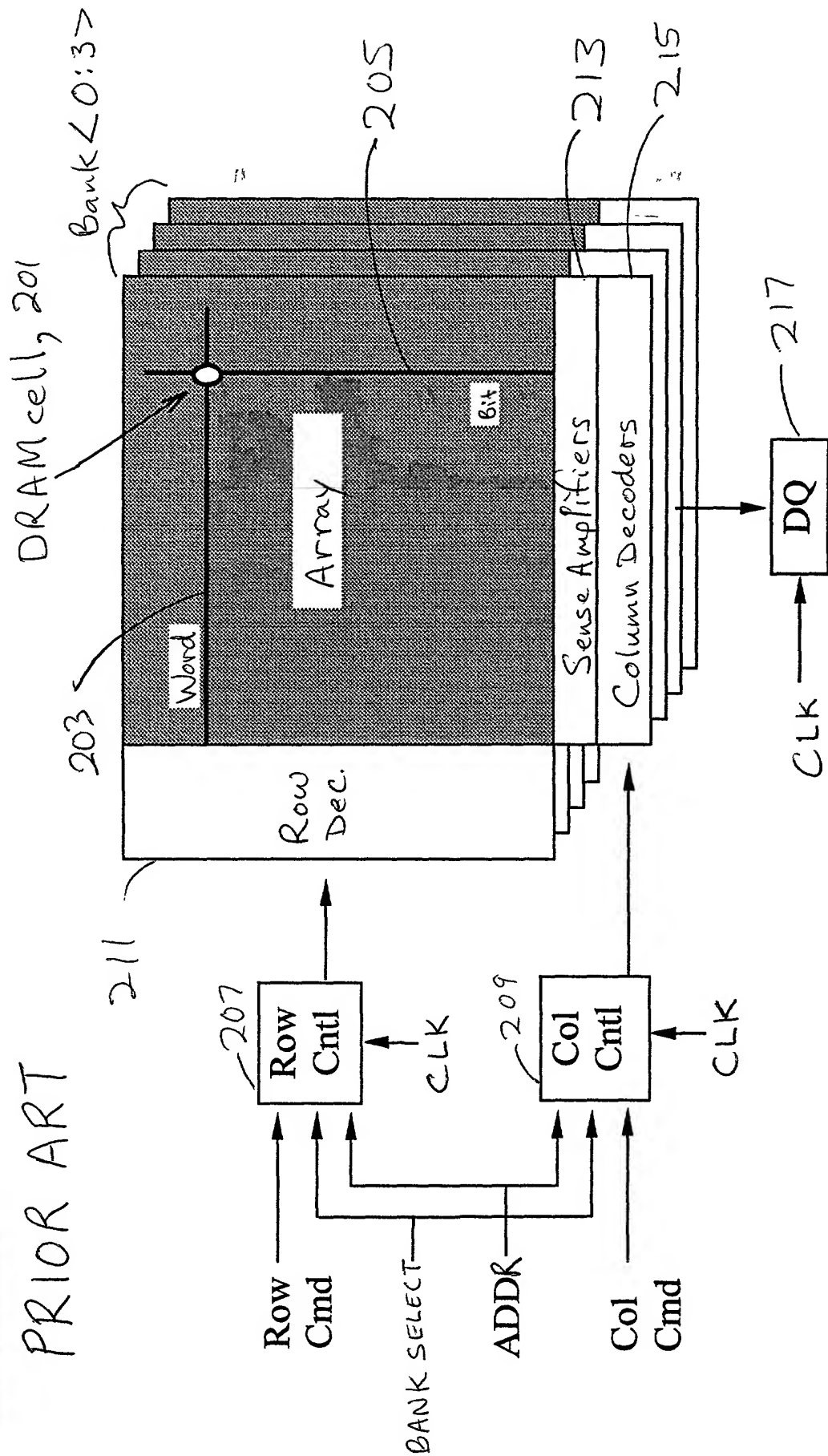


Figure 3: ECC enabled array block diagram

